

MPF DaVinci with Bbt Table User Manual

This scheme is a Multiple Partition format. Bad blocks within each partition do not affect the starting location of follow up partitions.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address: <http://www.dataio.com/support/dsr.asp>

Bad Block Handling Type = “MPF DaVinci with Bbt Table”

Spare Area = “ECC, Default = Disabled”

ECC Extended Type = “ECC DaVinci with Bbt (Large page), Default = ECC”

BBT Enabled = “Yes, Default = No”

Partition Table File = “Partition.MBN, Default = C:\Partition.MBN”

Image Preparation

The image data file should not contain any OOB/Spare area data. The algorithm will add the Spare area and related ECC data.

Partition Table Format Partition.mbn

- A binary file of YourFile.MBN with fixed length of 256 bytes.
- Organization: 16 rows x 4 columns. Each table item is 32-bits, little endian byte ordering.
- Each row of the table describes configuration for one partition. Up to 16 partitions can be used.
- Partition configuration:
 - i. **Start Adr:** address of start of partition in flash blocks. The programmer will set the file read pointer and the programmer write pointer to Start Adr. If Start Adr=0xFFFFFFFF, skip to the next partition.
 - ii. **End Adr:** last valid block in the current partition. The last data block programmed must be equal to or less than End Adr, otherwise the programmer will reject the flash device.
 - iii. **Actual Data Length:** number of blocks of data to read from the input file and write to the flash in the current partition.

Note – The last entry in the partition table should be allocated for the Bbt table. Its size for a device with n blocks should be: Start #: n- 4, End # n-1, Size: 4.

Revision History

V1.0 Date 09/22/2015

Appendix

You can get the file "Description of common NAND special features.pdf" from <http://ftp.dataio.com/FCNotes/BBM/>