

Skip PNX5100

General Description and Name

This method is based on SKIP with Multiple Partitions. The Partition mapping is based on the selected device size – see Table at the End of the document.

Spare Area ECC

Select “ECC Linux” to add the spare area information including the Linux ECC for the PNX5100. If the Spare area information is already in the Image file select Enabled.

File System

Fixed Block Locations

Special or Dynamic structures to be programmed by Data I/O

None for this scheme

Dynamic parameters that should be changeable by the customer

None for this scheme

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values are shown but customers are free to select any value they wish.

- Bad Block Handling Type = “**SKIP PNX5100**”

- Spare Area:
 - “ECC Linux” if spare area needs to be added including the Linux ECC
 - “Enabled” if ECC is already part of the Master Image

User Checksum Implications

If this scheme, or any bad blocking scheme is used, there are implications to the checksum of the image. As with any device, TLWin produces a checksum over the entire image rather than just the user data file.

Image Preparation

Nand layout is defined per size – Image Addresses are without spare area

- **16MByte** – Small Page - Nand offset (STM NAND128W3A, ...)

Partition	Start Block - dec	End Block - dec	Start Address
u-boot	0	39	0x00000
env bin	40	63	0xA0000
reserved1	64	87	0x100000
TM1	88	319	0x160000
TM2	320	415	0x500000
TM3	416	511	0x680000
reserved2	512	1023	0x800000

- **32MByte** - Small Page - Nand offset (STM NAND256W3A, ...)

Partition	Start Block - dec	End Block - dec	Start Address
u-boot	0	71	0x000000
env bin	72	127	0x120000
reserved1	128	191	0x200000
TM1	192	511	0x300000
TM2	512	703	0x800000
TM3	704	895	0xB00000
reserved2	896	2047	0xE00000

- **64MByte** - Small Page - Nand offset (STM NAND512W3A, ...)

Partition	Start Block - dec	End Block - dec	Start Address
u-boot	0	127	0x000000
env bin	128	255	0x200000
reserved1	256	383	0x400000
TM1	384	1023	0x600000
TM2	1024	1407	0x1000000
TM3	1408	1663	0x1600000
reserved2	1664	4095	0x1A00000

- **128MByte** - Small Page - Nand offset (STM NAND01GW3A, ...)

Partition	Start Block - dec	End Block - dec	Start Address
u-boot	0	191	0x000000
env bin	192	383	0x300000
reserved1	384	1023	0x600000
TM1	1024	1663	0x1000000
TM2	1664	2047	0x1A00000
TM3	2048	2303	0x2000000
reserved2	2304	8191	0x2400000

- **128MByte** - Large Page - Nand offset (STM NAND01GW3B, ...)

Partition	Start Block - dec	End Block - dec	Start Address
u-boot	0	23	0x000000
env bin	24	47	0x300000
reserved1	48	127	0x600000
TM1	128	207	0x1000000
TM2	208	255	0x1A00000
TM3	256	287	0x2000000
reserved2	288	1023	0x2400000

- **256MByte** - Large Page - Nand offset (STM NAND02GW3B, ...)

Partition	Start Block - dec	End Block - dec	Start Address
u-boot	0	79	0x000000
env bin	80	127	0xA00000
reserved1	128	255	0x1000000
TM1	256	383	0x2000000
TM2	384	511	0x3000000
TM3	512	639	0x4000000
reserved2	640	2047	0x5000000